

Abstracts

Broadband tuner on a chip for cable modem, HDTV, and legacy analog standards

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Market requirements and implementation of the first broadband tuner on a chip are described. This IC is built in a 0.8-/spl mu/m BICMOS process and includes the entire signal path as well as integrated PLLs. The architecture and design are discussed at the block and transistor levels. Measured results are shown.

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