

# Abstracts

## Broadband tuner on a chip for cable modem, HDTV, and legacy analog standards

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*B. Taddiken, W. Ezell, E. Mumper, K. Clayton, J. Douglass, V. Birleson, J. Esquivel, O. Werther, A. Schneider, J. Bolton, B. White, G. Dawe, M. Lafuente, K. Richter, J. Michon, J. Ruiz and J. Norsworthy. "Broadband tuner on a chip for cable modem, HDTV, and legacy analog standards." 2000 Radio Frequency Integrated Circuits (RFIC) Symposium 00. (2000 [RFIC]): 17-20.*

Market requirements and implementation of the first broadband tuner on a chip are described. This IC is built in a 0.8- $\mu\text{m}$  BICMOS process and includes the entire signal path as well as integrated PLLs. The architecture and design are discussed at the block and transistor levels. Measured results are shown.

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